

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1934	726/26.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L2	1071	L1 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L3	17	L2 and (JTAG (joint adj test adj action adj group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L4	1	10/706365.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L5	0	("2005/0099832").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L6	17	L2 and (JTAG (joint adj3 test adj3 action adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L7	51364	(JTAG (joint adj3 test adj3 action adj3 group) PLD (programmable near3 logic near3 device))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L8	7804	(JTAG (joint adj3 test adj3 action adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L9	130342	"455"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L10	106	L8 and L9	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2009/01/30 13:21
L11	44871	PLD (programmable near3 logic near3 device)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L12	38	L10 and L11	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L13	17	L2 and (JTAG (joint adj3 test adj3 (action access) adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L14	7808	(JTAG (joint adj3 test adj3 (action access) adj3 group))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21

L15	1858	(JTAG (joint adj3 test adj3 (action access) adj3 group)) near3 port	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L16	5	L1 and L15	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L17	435	L11 and L15	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L18	1844	(IC (integrated circuit)) near5 ((test\$3 jtag (joint near3 action)) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L19	20287	(prevent\$3 near3 (reprogram\$4 modification modif\$4 reconfigur\$5))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L20	9	L18 and L19	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L21	2211	Xilinx.as.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L22	1609	L21 and L11	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L23	13	L18 and L22	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L24	1	08/512796.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L25	1	08/670916.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L26	19	("5764076").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L27	1	08/670472.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L28	1	08/533412.app.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L29	20	("5689516").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L30	3	("7058856").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L31	2164	inhibit near3 (bit nand gate circuitry) same (one near3 time near3 programmable register)	USPAT	OR	ON	2009/01/30 13:21

L32	696	(inhibit near3 (bit circuitry)) same (one near3 time near3 programmable register)	USPAT	OR	ON	2009/01/30 13:21
L33	270	(inhibit near3 bit) with (one near3 time near3 programmable register)	USPAT	OR	ON	2009/01/30 13:21
L34	4	L18 and L33	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L35	0	(inhibit near3 bit) with (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2009/01/30 13:21
L36	0	(inhibit near3 (bit circuitry)) same (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2009/01/30 13:21
L37	68	(one near3 time near3 programmable near3 register)	USPAT	OR	ON	2009/01/30 13:21
L38	4	L18 and L33	USPAT	OR	ON	2009/01/30 13:21
L39	1	L18 and L37	USPAT	OR	ON	2009/01/30 13:21
L40	4906	((one near3 time near3 programmable)(one near3 time)(non near3 programmable)eprom) near3 register	USPAT	OR	ON	2009/01/30 13:21
L41	46	L15 and L40	USPAT	OR	ON	2009/01/30 13:21
L42	0	(inhibit near3 (bit nand gate circuitry)) with ((test \$3 JTAG) near3 port) with (enabl\$4 desabl\$4)	USPAT	OR	ON	2009/01/30 13:21
L43	9563	(inhibit near3 (bit nand gate circuitry))	USPAT	OR	ON	2009/01/30 13:21
L44	1130	((test\$3 JTAG) near3 port) same enabl\$4 desabl\$4	USPAT	OR	ON	2009/01/30 13:21
L45	0	L43 same L44	USPAT	OR	ON	2009/01/30 13:21
L46	25	L43 and L44	USPAT	OR	ON	2009/01/30 13:21
L47	0	(inhibit near3 (bit nand gate circuitry)) near3 (modifiable)	USPAT	OR	ON	2009/01/30 13:21

L48	0	(inhibit near3 (bit nand gate circuitry)) with (modifiable)	USPAT	OR	ON	2009/01/30 13:21
L49	1	(inhibit near3 (bit nand gate circuitry)) same (modifiable)	USPAT	OR	ON	2009/01/30 13:21
L50	195	(inhibit near3 (bit nand gate circuitry)) same (modif \$5)	USPAT	OR	ON	2009/01/30 13:21
L51	62	(inhibit near3 (bit nand gate circuitry)) with (modif \$5)	USPAT	OR	ON	2009/01/30 13:21
L52	19	(inhibit near3 (bit nand gate circuitry)) near3(modif \$5)	USPAT	OR	ON	2009/01/30 13:21
L53	0	L4 and L50	USPAT	OR	ON	2009/01/30 13:21
L54	0	L44 and L50	USPAT	OR	ON	2009/01/30 13:21
L55	50	(inhibit near3 (bit nand gate circuitry)) near3(modif \$5 updat\$3)	USPAT	OR	ON	2009/01/30 13:21
L56	1	((inhibit near3 (bit nand gate circuitry)) near3(modif \$5 updat\$3)) same configur \$5	USPAT	OR	ON	2009/01/30 13:21
L57	4	((inhibit near3 (bit nand gate circuitry)) with (modif \$5 updat\$3)) same configur \$5	USPAT	OR	ON	2009/01/30 13:21
L58	0	L18 and L50	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L59	231	(inhibit near3 (bit nand gate circuitry signal\$1)) with (modif\$5)	USPAT	OR	ON	2009/01/30 13:21
L60	0	L44 and L55	USPAT	OR	ON	2009/01/30 13:21
L61	0	L18 and L55	USPAT	OR	ON	2009/01/30 13:21
L62	21510	(inhibit near3 (bit nand gate circuitry signal\$1))	USPAT	OR	ON	2009/01/30 13:21
L63	2449	((test\$3 JTAG) near3 port) same (enabl\$4 allow\$3)	USPAT	OR	ON	2009/01/30 13:21
L64	115	L62 and L63	USPAT	OR	ON	2009/01/30 13:21
L65	1230	((test\$3 JTAG) near3 port) with(enabl\$4 allow\$3)	USPAT	OR	ON	2009/01/30 13:21

L66	625	((test\$3 JTAG) near3 port) with(enabl\$4 disabl\$3)	USPAT	OR	ON	2009/01/30 13:21
L67	62	((test\$3 JTAG) near3 port) with (enabl\$4 and disabl\$3)	USPAT	OR	ON	2009/01/30 13:21
L68	4	((test\$3 JTAG) near3 port) with enabl\$4) same inhibit	USPAT	OR	ON	2009/01/30 13:21
L69	3	("6523099").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L70	508	L40 and (inhibit near3 (circuitry signal circuit gate))	USPAT	OR	ON	2009/01/30 13:21
L71	1	(L40 and (inhibit near3 (circuitry signal circuit gate))).ab.	USPAT	OR	ON	2009/01/30 13:21
L72	0	L70 and (baseband near3 chip)	USPAT	OR	ON	2009/01/30 13:21
L73	11	L70 and baseband	USPAT	OR	ON	2009/01/30 13:21
L74	5	L40 with (inhibit near3 (circuitry signal circuit gate))	USPAT	OR	ON	2009/01/30 13:21
L75	1642	726/24,26.ccls. or 714/716,725.ccls.	USPAT	OR	ON	2009/01/30 13:21
L76	0	L75 and ((inhibit near3 circuitry) same (((test\$3 JTAG) near3 port))with enabl\$3)	USPAT	OR	ON	2009/01/30 13:21
L77	0	L75 and ((inhibit near3 circuitry) and(((test\$3 JTAG) near3 port))with enabl\$3)	USPAT	OR	ON	2009/01/30 13:21
L78	4	((inhibit near3 circuitry) and(((test\$3 JTAG) near3 port))with enabl\$3)	USPAT	OR	ON	2009/01/30 13:21
L79	4	((inhibit near3 circuitry) and(((test\$3 JTAG) near3 port))same enabl\$3)	USPAT	OR	ON	2009/01/30 13:21
L80	223	(inhibit near3 (circuitry gate signal)) and ((test\$3 JTAG) near3 port)	USPAT	OR	ON	2009/01/30 13:21
L81	11	("20050039039"   "4698750"   "5689516"   "5737760"   "5819025"   "6145122"   "6243842"   "6523099"   "6662314"   "6711684"   "6769065").PN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/01/30 13:21

L82	89	("4698750").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L83	25	(johannes near3 becker). in.	USPAT	OR	ON	2009/01/30 13:21
L84	1472	(agere near3 inc).as.	USPAT	OR	ON	2009/01/30 13:21
L85	12	(johannes adj becker).in.	USPAT	OR	ON	2009/01/30 13:21
L86	25	(johannes near2 becker). in.	USPAT	OR	ON	2009/01/30 13:21
L87	2165	(agere near3 systems).as.	USPAT	OR	ON	2009/01/30 13:21
L88	10	L87 and JTAG	USPAT	OR	ON	2009/01/30 13:21
L89	6020	(mobile wireless pda mda (mp3 near3 player) (set near top)) same (((one single) near3 time near3 program\$5) eprom (non near3 programmable))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L90	43	((mobile wireless pda mda (mp3 near3 player) (set near top)) same (((one single) near3 time near3 program\$5) eprom (non near3 programmable))) .ab.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L91	32	((mobile wireless pda mda (mp3 near3 player) (set near top)) same (((one single) near3 time near3 program\$5)(non near3 programmable))) .ab.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L92	385	((mobile pda mda (mp3 near3 player)) same (((one single) near3 time near3 program\$5)(non near3 programmable)))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L93	413	((one single) near3 time near3 program\$5)(non near3 programmable)) near3 register	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L94	630328	(mobile pda mda (mp3 near3 player))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L95	6	L93 same L94	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L96	83	L93 and L94	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21

L97	0	L96 and (((test\$3 JTAG) near3 port) same enabl\$4)	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2009/01/30 13:21
L98	0	L96 and ((test\$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2009/01/30 13:21
L99	1	L96 and (((test\$3 JTAG) near3 port) same enabl\$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L100	0	("2005/0099832").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L101	6	L96 and (((test\$3 JTAG) near3 port) and enabl\$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L102	6	L96 and ((test\$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L103	26	L93 and ((test\$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L104	22	("5838901").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L105	19	("5764076").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L106	11	("20050039039"   "4698750"   "5689516"   "5737760"   "5819025"   "6145122"   "6243842"   "6523099"   "6662314"   "6711684"   "6769065"). PN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2009/01/30 13:21
L107	1	"5199032".pn.	USPAT	OR	ON	2009/01/30 13:21
L108	39	("5199032").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L109	7759	Ericsson.as.	USPAT	ADJ	ON	2009/01/30 13:21
L110	4	L109 and (jtag)	USPAT	ADJ	ON	2009/01/30 13:21
L111	0	xilinx.as. and (JGAT)	USPAT	ADJ	ON	2009/01/30 13:21
L112	1949	xilinx.as.	USPAT	ADJ	ON	2009/01/30 13:21
L113	179	L112 and JTAG	USPAT	ADJ	ON	2009/01/30 13:21
L114	0	L112 and (inhibit JTAG)	USPAT	ADJ	ON	2009/01/30 13:21

L115	223	L112 and (inhibit JTAG)	USPAT	OR	ON	2009/01/30 13:21
L116	2	L112 and (inhibit and JTAG)	USPAT	OR	ON	2009/01/30 13:21
L117	948	714/30.ccls.	USPAT	ADJ	ON	2009/01/30 13:21
L118	235	L117 and JTAG	USPAT	ADJ	ON	2009/01/30 13:21
L119	132	L118 and tap	USPAT	ADJ	ON	2009/01/30 13:21
L120	23	L119 and inhibit	USPAT	ADJ	ON	2009/01/30 13:21
L121	1934	726/26.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L122	1071	L121 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L123	10795	((test\$3 JTAG) near3 port) and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L124	231	(inhibit near3 (bit nand gate circuitry signal\$1)) with (modif\$5)	USPAT	OR	ON	2009/01/30 13:21
L125	0	L124 and L123	USPAT	OR	ON	2009/01/30 13:21
L126	21510	(inhibit near3 (bit nand gate circuitry signal\$1))	USPAT	OR	ON	2009/01/30 13:21
L127	251	L123 and L126	USPAT	OR	ON	2009/01/30 13:21
L128	1511	(port near3 access near3 circuitry) (tap near3 controller)	USPAT	OR	ON	2009/01/30 13:21
L129	57	L127 and L128	USPAT	OR	ON	2009/01/30 13:21
L130	84	((test\$3 JTAG) near3 port) same (port near3 access near3 circuitry)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L131	7	L129 and L130	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L132	2	("6381721").URPN.	USPAT	ADJ	ON	2009/01/30 13:21
L133	519	714/727.ccls.	USPAT	ADJ	ON	2009/01/30 13:21
L134	309	L133 and ((test\$3 JTAG) near3 port)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21



L135	12	L126 and L134	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L136	12	L135 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L137	12	L136 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L138	0	inhibit near3 (bit nand gate circuitry) with (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2009/01/30 13:21
L139	0	inhibit near3 (bit nand gate circuitry) same(one near3 time near3 programmable near3 register)	USPAT	OR	ON	2009/01/30 13:21
L140	0	inhibit same(one near3 time near3 programmable near3 register)	USPAT	OR	ON	2009/01/30 13:21
L141	1	inhibit and (one near3 time near3 programmable near3 register)	USPAT	OR	ON	2009/01/30 13:21
L142	0	loopback near3 input near3 output near3 pin\$1	USPAT	OR	ON	2009/01/30 13:21
L143	1	loop near3 back near3 input near3 output near3 pin\$1	USPAT	OR	ON	2009/01/30 13:21
L144	1	"6625472".pn.	USPAT	OR	ON	2009/01/30 13:21
L145	112	loop near3 back near3 pin \$1	USPAT	OR	ON	2009/01/30 13:21
L146	50	loopback near3 pin\$1	USPAT	OR	ON	2009/01/30 13:21
L147	3575	baseband with mobile	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2009/01/30 13:21
L148	3871	(726/24,26.ccls.) (714/716,725,727.ccls.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L149	5	L147 and L148	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L150	159	(baseband near3 chip) with mobile	US-PGPUB; USPAT; EPO; JPO	ADJ	ON	2009/01/30 13:21
L151	150	L150 and (IC (integrated circuit))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21

L152	150	((baseband near3 chip with mobile) and (IC (integrated circuit)))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2009/01/30 13:21
L153	92	(enabl\$3) near3 (test\$3 jtag (joint adj test)) adj port	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L154	20	(port near3 inhibit) with (configuration configur\$3 modify\$3 modifiable)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L155	1	L153 and L154	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L156	7196	inhibit with (configuration configur\$3 modify\$3 modifiable)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L157	1	L153 and L156	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L158	19531	inhibit\$3 with (configuration configur\$3 modify\$3 modifiable)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L159	4694	(inhibit\$3 prohibit\$3 allow\$3) near3 (bit signal) with (configuration configur\$3 modify\$3 modifiable)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L160	5	L153 and L159	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L161	17	(enabl\$3) near3 (test\$3 jtag (joint adj test)) adj port same (inhibit\$3 prohibit\$3 allow\$3) near3 (bit signal)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L162	25	(enabl\$3) near3 (test\$3 jtag (joint adj test)) adj port same (inhibit\$3 prohibit\$3 allow\$3)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L163	8	L162 not L161	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L164	115	(test\$3 jtag (joint adj test)) adj port same (inhibit\$3 prohibit\$3 allow\$3) near3 (bit signal circuitry)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L165	7	(test\$3 jtag (joint adj test)) adj port same (inhibit\$3 prohibit\$3) near3 (bit signal circuitry)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L166	615	(test\$3 jtag (joint adj test)) adj port same (inhibit\$3 prohibit\$3 control\$4) near3 (bit signal circuitry input)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21

L167	358	(test\$3 jtag (joint adj test)) adj port same (inhibit\$3 prohibit\$3 control\$4) near3 (bit signal circuitry input)	USPAT	OR	ON	2009/01/30 13:21
L168	260	L167 and ((integrated near3 circuit\$2) ic)	USPAT	OR	ON	2009/01/30 13:21
L169	21	L168 and (port near3 access\$3 near3 circuit\$2)	USPAT	OR	ON	2009/01/30 13:21
L170	724	(enabl\$3) near3 (test\$3 jtag (joint adj test)) adj (port circuit\$2)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L171	6	L170 and L159	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L172	1	L171 not L160	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L173	34	((otp (one adj2 time)) adj3 register) same second near3 bit	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L174	1	restrict\$3 with certain near3 function\$5 same jtag	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L175	0	jtag same (partially near3 enabl\$3)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L176	13	jtag and (partially near3 enabl\$3)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L177	51	((partially partly) near3 (enabl\$3 disabl\$3)) with port	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L178	0	L177 and jport	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L179	1	L177 and ((otp (one adj2 time)) adj3 register)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L180	1	((partially partly) near3 (enabl\$3 disabl\$3)) with (test near3 port)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L181	78	inhibit\$4 with partially with port	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L182	8	L181 and (integrated near3 circuit (IC))	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L183	0	(inhibit\$4 support\$3 prevent\$3) with partially with test\$3 adj port	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L184	3	(inhibit\$4 support\$3 prevent\$3) with partially with test\$3 near3 port	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21

L185	5	(test\$3 jtag) adj2 port same (reduced near3 function\$5)	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L186	1	direct near3 loopback with tdi with tdo	US-PGPUB; USPAT	OR	ON	2009/01/30 13:21
L187	0	(inhibit\$3 near3 (second\$4 multiple) adj3 bit) near3 (modif\$5 updat\$3)	USPAT	OR	ON	2009/01/30 13:21
L188	0	(inhibit\$3 near3 (second\$4 multiple) adj3 bit) near3 (modif\$5 updat\$3)	USPAT	OR	ON	2009/01/30 13:21
L189	0	(inhibit\$3 near3 (bit circuitry)) same (one near3 time near3 programmable)	USPAT	OR	ON	2009/01/30 13:21
L190	3	(inhibit\$3 near3 (second\$4 multiple) adj3 bit) with register	USPAT	OR	ON	2009/01/30 13:21
L191	0	("2005/0099832").URPN.	USPAT	OR	ON	2009/01/30 13:21
L192	4	multiple near3 bit same ((test\$3 near3 port) jtag) same (enabl\$3 disabl\$3 restrict\$3)	USPAT	OR	ON	2009/01/30 13:21
L193	2	((test\$3 near3 port) jtag) same (enabl\$3 disabl\$3 restrict\$3) with partially	USPAT	OR	ON	2009/01/30 13:21
L194	1	"7159118".pn.	USPAT	OR	ON	2009/01/30 13:21
L195	754	((test\$3 JTAG) near3 port) near5 (enabl\$4 allow\$3)	USPAT	OR	ON	2009/01/30 13:21
L196	79	((test\$3 JTAG) near3 port) near5 disabl\$3	USPAT	OR	ON	2009/01/30 13:21
L197	44	L195 and L196	USPAT	OR	ON	2009/01/30 13:21
L198	1	((test\$3 JTAG) near3 port) with ((partial\$2 (some near3 extent) slightly) near3 disabl\$3)	USPAT	OR	ON	2009/01/30 13:21
L199	5	(port with ((partial\$2 (some near3 extent) slightly) near3 disabl\$3))	USPAT	OR	ON	2009/01/30 13:21

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